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### MAR 0 1 2005

Our Reference: 2024.47

PATENT

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lowrey, et al

Application Number: 10/801,414 Filing Date: March 16, 2004

Art Unit/Examiner: 2814/Pham, Hoai V.

Title: METHOD FOR MAKING PROGRAMMABLE RESISTANCE MEMORY

ELEMENT

#### AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is a response to the Office Action dated December 1, 2004 to which a response is due on or before March 1, 2005. The Commissioner is hereby authorized to charge any fees which may be required during the entire pendency of the application to Deposit Account No. 05-1068.

Application Number: 10/801,414

PAGE 2/11 \* RCVD AT 3/1/2005 4:41:02 PM [Eastern Standard Time] \* SVR:USPTO-EFXRF-1/3 \* DNIS:8729306 \* CSID:2488442273 \* DURATION (mm-ss):02-28

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## PATENT APPLICATION FEE DETERMINATION RECORD Effective October 1, 2003

Application or Docket Number

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